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2/28/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10086174	FILING DATE 02/28/2002	CLASS 345	SUBCLASS 556	GAU 267	EXAMINER <i>PHILLIPS Chauhan</i>
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**APPLICANTS: Lavelle Michael; Kubalska Ewa; Tang Yan;

**CONTINUING DATA VERIFIED:

FOREIGN APPLICATIONS VERIFIED:

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PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO 5181-86900
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			
Verified and Acknowledged Examiners's initials			
TITLE : Active block write-back from SRAM cache to DRAM			

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	
		Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheet Drwg.	Fig. Drwg.
		Print Fig.	
TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
		PREPARED FOR ISSUE	
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